

GP 2815

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. Sugawara, et al.

Serial No.: 09/733,474

Filed: December 8, 2000

For: SEMICONDUCTOR AND FABRICATION  
METHOD THEREOF

Case No.: 9792909-4740

Group Art Unit: 2815

Examiner: J. Nguyen

Date: December 19, 2001

Certificate of Mailing (37 CFR 1.8(a))

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to:

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Assistant Commissioner for Patents  
Washington, D.C. 20231, on:

Date of Deposit: December 19, 2001

 12/19/01  
JoEllen Hogan Date

**TRANSMITTAL LETTER**

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Enclosed herewith is a Response to Restriction Requirement of M. Sugawara, et al. in the above-identified patent application entitled SEMICONDUCTOR AND FABRICATION METHOD THEREOF.

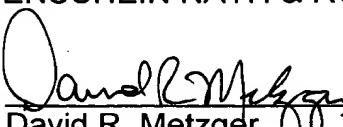
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The Commissioner is hereby authorized to charge any additional fees required, as well as any patent application processing fees associated with this communication for which full payment has not been tendered, to Deposit Account No. 19-3140. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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*JoEllen Hogan* *12/19/01*  
JoEllen Hogan Date

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**RESPONSE TO RESTRICTION REQUIREMENT**

Box Non-Fee Amendment  
Asst. Commissioner of Patents  
Washington, D.C. 20231

SIR:

This amendment is being filed in response to the Office Action dated December 7, 2001. Please reconsider the application in view of the amendment and remarks presented below.

**ELECTION OF CLAIMS**

In the Office Action, the Examiner requested an election of claims pursuant to a restriction requirement under 35 U.S.C. §121 between Group I claims 1-2, and Group II claim 3.

In response, applicant hereby elects examination of Group I claims 1-2.

Respectfully submitted,

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